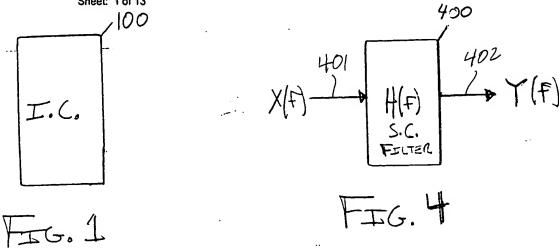
Title: METHODS, SYSTEMS, AND APPARATUS FOR INTEGRATED

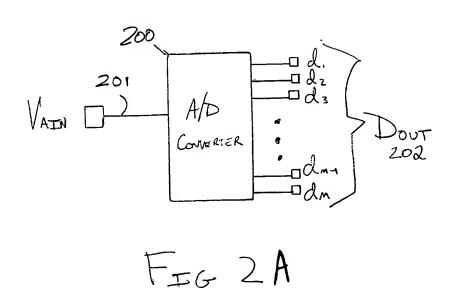
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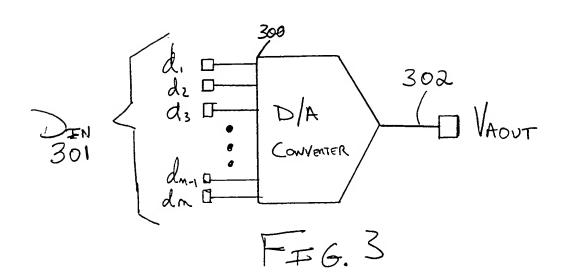
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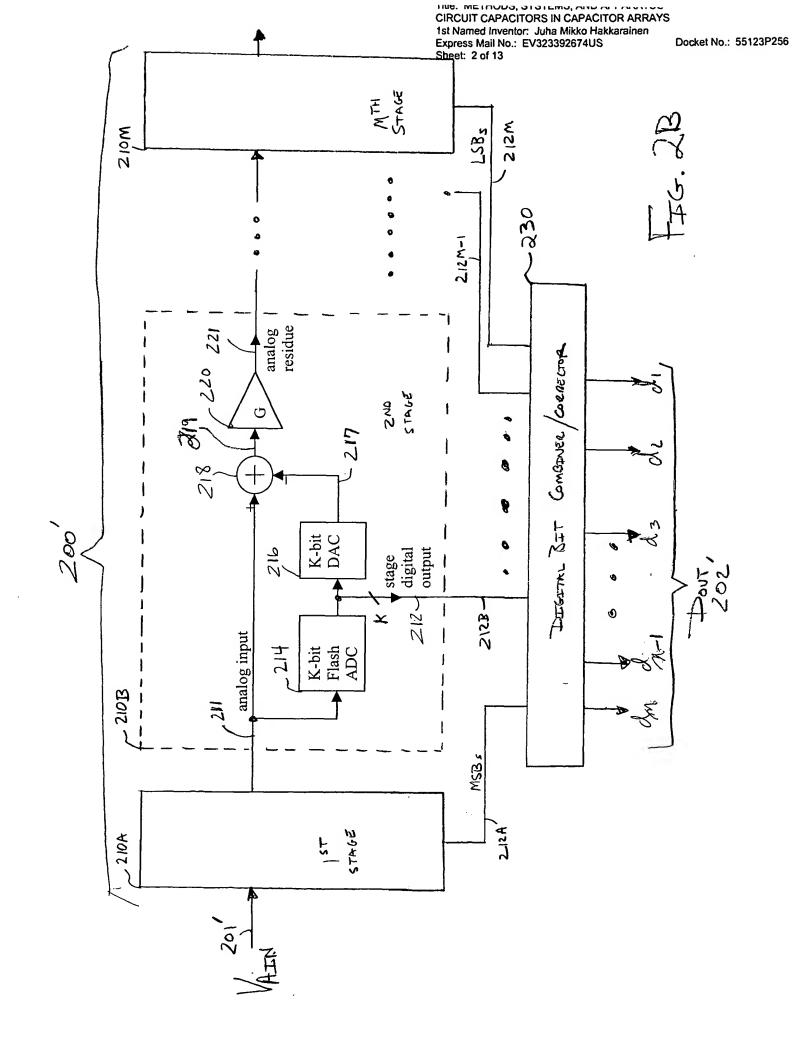
Docket No.: 55123P256

Sheet: 1 of 13







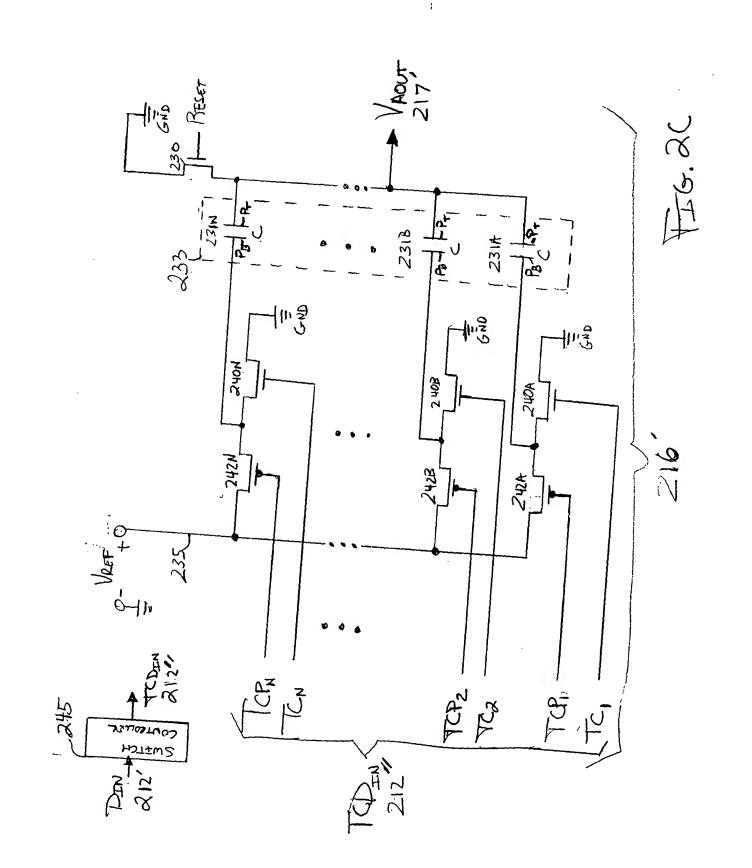


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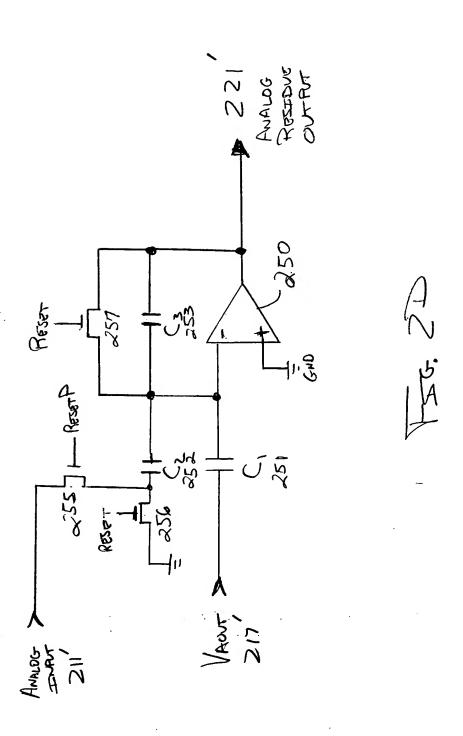
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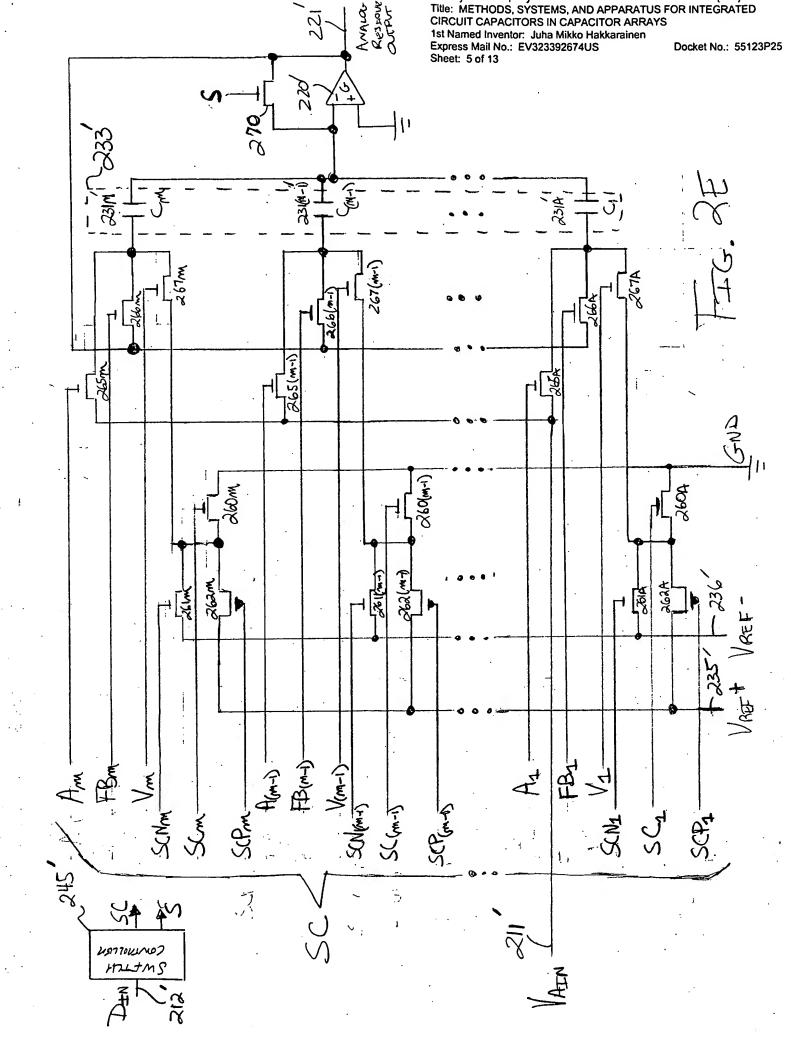
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Docket No.: 5512:
Sheet: 4 of 13

Docket No.: 55123P256







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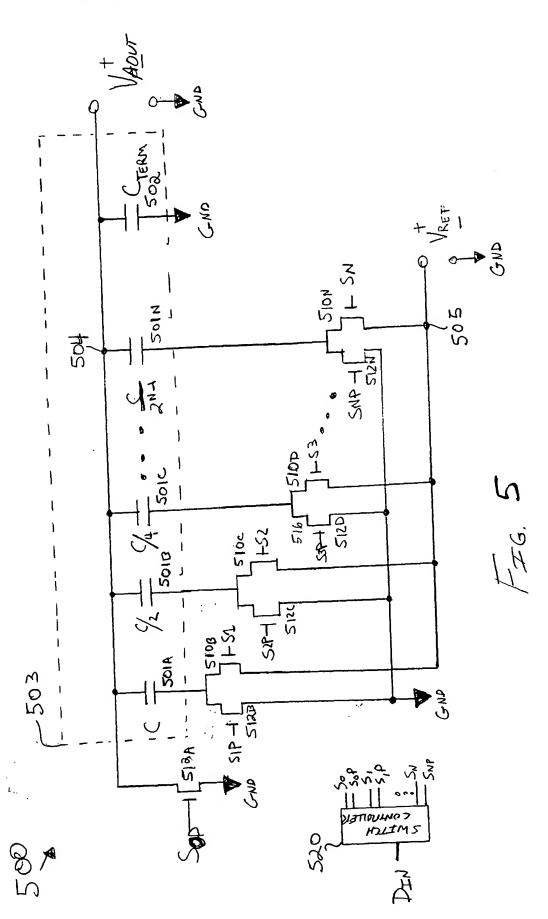
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Sheet: 6 of 13

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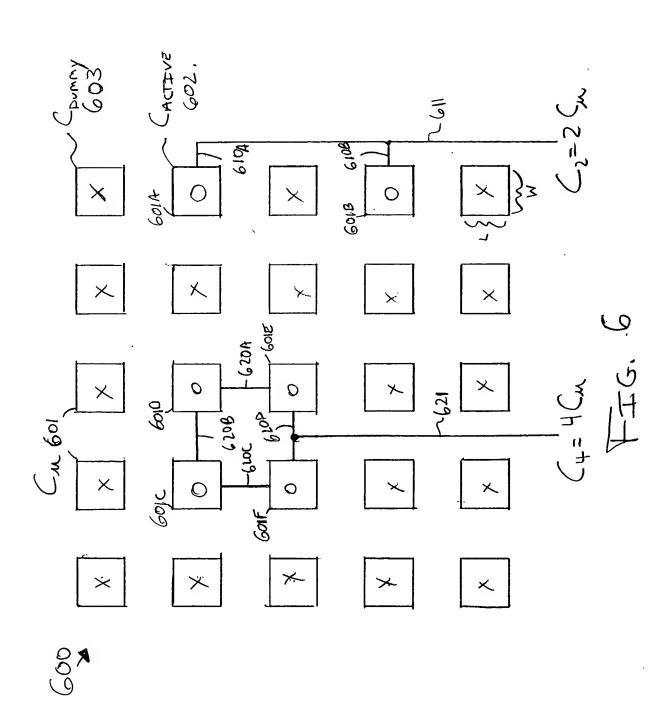
Blakely, Sokoloff, Taylor & Zafman LLP (714) 557-3800 Title: METHODS, SYSTEMS, AND APPARATUS FOR INTEGRATED

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Sheet: 7 of 13

Docket No.: 55123P256



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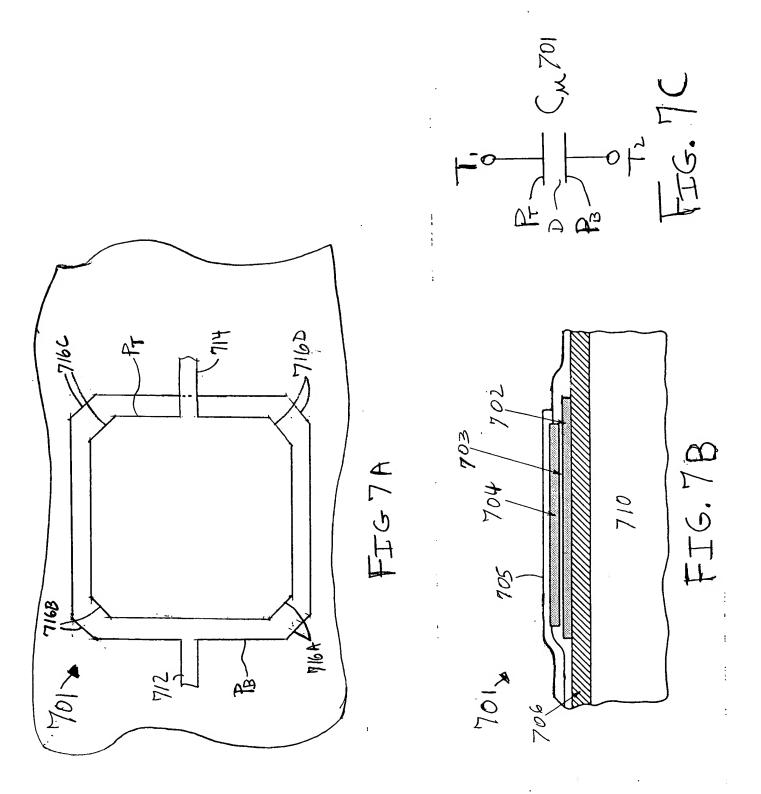
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Docket No.: 55123P256

Sheet: 8 of 13

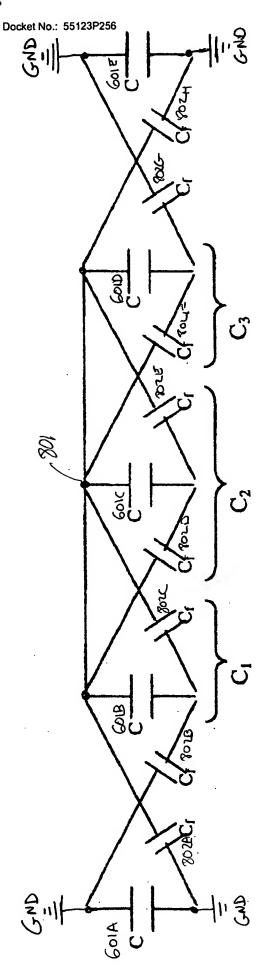


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Sheet: 9 of 13



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CIRCUIT CAPACITORS IN CAPACITOR ARRAYS 1st Named Inventor: Juha Mikko Hakkarainen

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Sheet: 10 of 13

Docket No.: 55123P256

X X X X X O O X X X X X FIG 9A

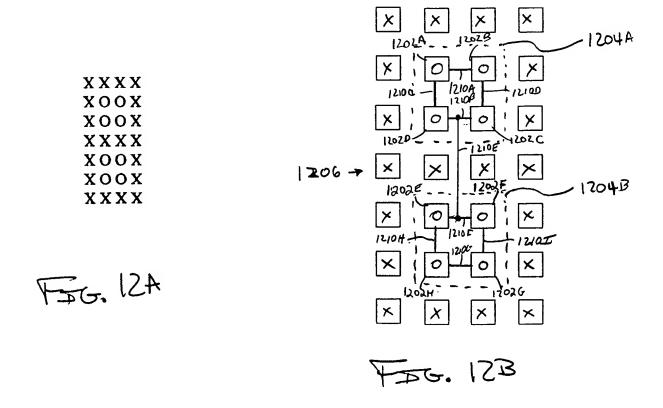
FIG. 9B

X X X X X O O X X O O X X X X X

FIG. 10A

FEG. LOB

Blakely, Sokoloff, Taylor & Zafman LLP Title: METHODS, SYSTEMS, AND APPARATUS FOR INTEGRATED CIRCUIT CAPACITORS IN CAPACITOR ARRAYS 1st Named Inventor: Juha Mikko Hakkarainen Docket No.: 55123P256 Express Mail No.: EV323392674US Sheet: 11 of 13 K × X 1101A 110A 110LB 1104A XXXX XOOX 1106A > X × X × XXXX 1101 D 1103C 110B XOOXXXXX Х X 0 0 XOOX 1104B XXXX × 1106B -1102F 1102E 11040 Hoc FIG. 11A X X × × FIG. 11B



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X

×

FFG. 13A

FIG 13B

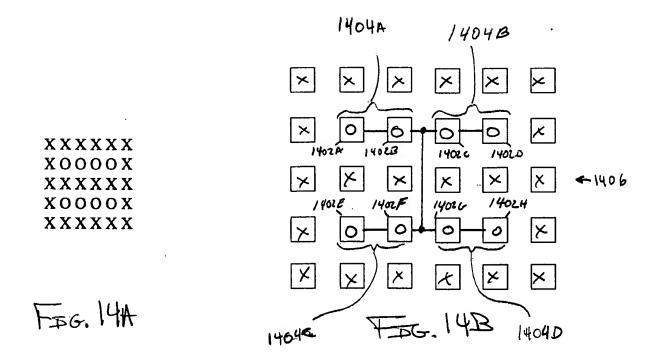
X

X

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X



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